EAST Search History

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		(processor microprocessor computer microcomputer controller microcontroller cpu) and (bus\$3 with communicat\$5 with (error fault abnormality abnormal unusual) with detect\$5) and (time delay timer\$2) and compar\$5 and comparator and register and threshold and bus and (compar\$4 with tim\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/24
L1	548	(timer counter clock) with bus with (abnormal \$4 error fault) with (measur\$4 sens\$4 detect\$5)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 16:53
L2	18	(timer counter clock) with bus with (abnormal \$4 error fault) with (measur\$4 sens\$4 detect\$5) same threshold	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 16:54
L3	1	(timer counter clock) with bus with (abnormal \$4 error fault) with (measur\$4 sens\$4 detect\$5) same threshold same select \$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 17:00
L4	23	\$4 error fault) with (measur\$4 sens\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 17:00
L5	348	702/120.CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 17:21

L6	164	702/120.CCLS. and bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 17:33
L7	55	702/120.CCLS. and bus. clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/11 17:33
S1	68		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:08
S2	3	(Communication\$3 with	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/14 16:04
S 3	133		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 14:11
S4	0	and(abnormal\$4 with detect\$4) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/14 16:38
S5	0	"4736366" and (abnormal\$4 with detect \$4) and (Communication\$3 with bus\$3) and timer\$2 and comparator\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 14:13
S6	1	"4736366" and (abnormal\$4 with detect \$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 14:12
S7	1	"4736366" and (abnormal\$4 with detect \$4) and (Communication\$3 with bus\$3) and timer\$2 and compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 14:26
S8	1		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:12

S9	70	Microcomputer and (abnormal\$4 with detect \$4) and (Communication\$3 with bus\$3) and timer\$2 and compar\$5 and regist\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:16
S10	1551		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:23
S11	2014	Microcomputer with bus \$3 and timer\$2 and compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:25
S12	1	702/120.ccls. and (Microcomputer with bus\$3 and timer\$2 and compar\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:26
S13	76	"702"/.ccls. and (Microcomputer with bus\$3 and timer\$2 and compar\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/20 15:26
S14	1	"4736366" and threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/21 13:51
S15	6	"4736366" and compar \$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/21 13:52
S16	29	"875227"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/24 14:56
S17	2	"09875227"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/24 14:56
S18	72	(processor microprocessor computer microcomputer controller microcontroller cpu) and (bus\$3 with communicat\$5 with (error fault abnormality abnormal unusual) with detect\$5) and (time delay timer\$2) and compar\$5 and comparator and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/24 16:34

S19	9	(processor microprocessor computer microcomputer controller microcontroller cpu) and (bus\$3 with communicat\$5 with (error fault abnormality abnormal unusual) with detect\$5) and (time delay timer\$2) and compar\$5 and comparator and register and threshold and bus and (compar\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/24 16:45
S20	34	with (time timing)) (abnormal\$4 with detect \$4) with (Communication\$3 with bus\$3) and tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/11 09:58
S21	19	13.3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/11 10:01
S22	187	((error abnormal\$4) with detect\$4) with (Communication\$3 with bus\$3) and tim\$4 and (threshold value point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/11 10:01
S23	27	((error abnormal\$4) with detect\$4) with (Communication\$3 with bus\$3) with tim\$4 and (threshold value point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/11 10:49
S24	19	((error abnormal\$4) with detect\$4) with (Communication\$3 with bus\$3) with tim\$4 and (threshold value point) and compar\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/11 10:50
S25	0	(abnormal\$4 with detect \$4) with (Communication\$3 with bus\$3) AND ("4736366". PN.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 17:35
S26	2	"4736366".PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 17:35

S27	43	(abnormal\$4 near6 detect\$4)near10 (Communication\$3 with bus\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 15:01
S28	35	(abnormal\$4 near6 detect\$4)near10 (Communication\$3 near4 bus\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 15:01
S29	22	(abnormal\$4 near6 detect\$4)near10 (Communication\$3 near4 bus\$3) and (tim \$3 measur\$5 near3 tim \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 15:03
S30	3701	(abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:40
S31	140	"340"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:41
S32	266	"370"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:41
S33	15	"342"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:41
S34	12	"398"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:41
S35	51	"370"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 11:43
S36	0	"370"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus near6 logic near4 level same time same compar \$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 12:58

S37	0	"370"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) same bus same logic near4 level same time same compar \$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 12:58
S38	44	"370"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:04
S39	23	"370"/\$.ccis. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:07
S40	O	702/120.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:07
S41	11	"702"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:08
S42	2	"73"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:08

S43	9	"324"/\$.ccls. and (abnormal\$4 error fault threshold) near5 (sens \$4 detect\$4 determin\$5 measur\$5) near4 bus and logic near4 level and time and compar\$5 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 13:08
S44	21054	FUJITSU adj LIMITED	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 14:32
S45	84	(FUJITSU adj LIMITED). in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 14:33
S46	0	(FUJITSU adj LIMITED). in. and communication near4 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/17 14:33
S47	14	"5565856"	USPAT	OR	OFF	2005/08/31 16:05
S48	15	"5565856"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 17:13
S49	2	(abnormal\$4 near5 (measur\$5 sens\$5 detect\$5)) same communication\$3 adj3 bus\$2 same timer same comparator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 17:19
S50	2	"5565856".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:56
S51	117	(abnormal\$5 error\$3 defect\$4) with (measur \$5 sens\$5 detect\$5 test \$4) with communication adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 16:49
S52	5	(abnormal\$5 error\$3 defect\$4) with (measur \$5 sens\$5 detect\$5 test \$4) with communication adj bus same (timer counter) same (compar\$4 comparator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 16:55

S53	5		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/12 16:55
S54	5		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:24
S55	361		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:24
S56	216		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:24
S57	98		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:25
S58	10	defect\$4) same (measur	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:40
S59	483		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:40
S60	8		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:41

S61	490	((abnormal threshold error defect) near4 bus).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:57
S62	9	((abnormal threshold error defect) near4 communication adj bus). ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:57
S63	15	((abnormal threshold diagnos\$5 error defect) near4 communication adj bus).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 20:58
S64	8	"4613858"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:12
S65	304486	timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:13
S66	1181204	timer counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:13
S67	1275648	timer counter ((measur \$4 sens\$4 detect\$4) adj time\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:16
S68	590715	bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:16
S69	3851433	controller microcontroller processor microprocessor cpu computer microcomputer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:17
S70	130335	S67 and S68 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:18
S71	545654	compar\$5 with time	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:18
S72	33411	S70 and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:19

S73	1986716	error abnormal\$4 anomal\$4 defect\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:19
S74	22135	S72 and S73	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:20
S75	1993008	S74 and threshold limit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:20
S76	16510	S74 and (threshold limit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:21
S77	10404	S76 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:21
S78	10165	S77 and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:23
S79	8831	S78 and logic\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:24
S80	41882	"702"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:24
S81	337	"702"/\$.ccls. and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:25
S82	950	test same bus.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:25
S83	0	S79 and S82	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:26
S84	3287	communication with bus and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:26
S85	18	communication with bus.ti. and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:34

S86	5366	communication with bus.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:34
S87	2493	communication with bus and monitor\$4.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:35
S88	5507	communication and bus and monitor\$4.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:38
S89	1167	S88 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:35
S90	0	S89 and comnparator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:35
S91	250	S89 and comparator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:36
S92	158	S91 and compar\$4 with tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:36
S93	51	S91 and compar\$4 with tim\$4 same (threshold limit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:36
S94	35	(communication adj bus with monitor\$4).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:38
S95	7597839	S94 and compar\$4 tim \$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:38
S96	2	S94 and compar\$4 same tim\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/14 15:38
S97	17	("3810120").PN. OR ("4149241").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/02/14 15:41
S98	3	S97 and compar\$4 with tim\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2007/02/14 15:47

S99	33	BARR near3 ANDREW	US-PGPUB;	OR	OFF	2007/02/14
		and communication with bus	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			15:59
S100	O	S99 and timer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 15:58
S101	0	BARR near3 ANDREW and communication with bus same (abnormal\$4 error anomaly)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 15:59
S102	7	BARR and communication with bus same (abnormal\$4 error anomaly)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 16:00
S103	0	BARR and communication with bus same (abnormal\$4 error anomaly) and timer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 16:00
S104	10	communication with bus same (abnormal\$4 error anomaly) same timer same compar\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 16:04
S105	4	"709"/\$.ccls. and communication with bus same (abnormal\$4 error anomaly) same timer same compar\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 16:04
S106	2719	(measur\$4 sens\$4 detect\$5) near5 (error \$3 abnormal\$5 anomal \$4) near4 bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 14:39
S107	59	(measur\$4 sens\$4 detect\$5) near5 (error \$3 abnormal\$5 anomal \$4) near4 bus with direct\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 14:42
S108	197	(measur\$4 sens\$4 detect\$5) near5 (error \$3 abnormal\$5 anomal \$4) near4 bus same direct\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 14:39

S109	7	(measur\$4 sens\$4 detect\$5) near5 (error \$3 abnormal\$5 anomal \$4) near4 bus with direct\$4 same time\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 14:43
S110	0	(measur\$4 sens\$4 detect\$5) near5 (error \$3 abnormal\$5 anomal \$4) near4 bus with direct\$4 same time\$3 same compar\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 14:43
S111	6137	(abnormal\$4 error anomal\$4) with (measur \$4 sens\$4 detect\$5) with bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/19 22:25
S112	411	((abnormal\$4 error anomal\$4) with (measur \$4 sens\$4 detect\$5) with bus).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/19 22:26
S113	12	S112 and (cpu computer microcomputer microprocessor microcontroller controller processor) with (seperate independent)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/19 22:29
S114	2403210	"6948100" an (tim\$4 counter) and compar \$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/23 16:11
S115	2	"6948100" and (tim\$4 counter) and compar \$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/23 16:11
S116	8754	(tim\$3 counter) same (abnormal\$4 error fault threshold)with bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/01 19:41
S117	4858	(tim\$3 counter) with (abnormal\$4 error fault threshold)with bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/01 19:41
S118	4056	(tim\$3 counter) with (abnormal\$4 error fault) with bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/01 19:41

S120	43		US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/01 19:56
S121	24	(tim\$3 counter) with (abnormal\$4 error fault) near5 bus with compar \$4 with (threshold limit value)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/01 19:56
S122	5	"5204904"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/02 17:42
S123	10	"5204954"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/03 13:48
S124	0	"5204954.pn."	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/03 13:48
S125	2	"5204954".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/03 13:48
S126	3	"5485580"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/18 10:47
S127	2	"5485580" and bus and (detect\$4 determin\$4 sens\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/18 10:49
S128	2	"5485580" and bus and (detect\$4 determin\$4 sens\$4) and high and low	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/19 17:36
S129	2	"5485580" and bus and (detect\$4 determin\$4 sens\$4) same abnormal \$4 and high and low and compar\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/19 18:05

S130	2	"5485580" and bus and (detect\$4 determin\$4 sens\$4) same abnormal \$4 and high and low and compar\$5 same logic\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/19 18:07
S131	2	"5485580" and bus and (detect\$4 determin\$4 sens\$4) same abnormal \$4 and (high low) same compar\$5 same logic\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:37
S132	0	"5485580" and bus and (detect\$4 determin\$4 sens\$4) same abnormal \$4 and (high low) same compar\$5 same logic\$3 same(threshold predetermin\$4 value)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:38
S133	1	"5485580" and bus and (detect\$4 determin\$4 sens\$4) same abnormal \$4 and (high low) same compar\$5 same logic\$3 and (timer counter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:50
S134	6	("4833600" "5043938" "5075569" "5126601" "5264744").PN. OR ("5485580").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/03/20 14:53
S135	0	S134 and (counter timer) same compar\$4 same (threshold value)	US-PGPUB; USPAT; USOCR	OR	ON	2008/03/20 15:01
S136	15775	(cpu controller microprocessor microcontroller microcomputer computer) same (counter timer) same compar\$4 same (threshold value)	US-PGPUB; USPAT; USOCR	OR	ON	2008/03/20 15:03
S137	104	(cpu controller microprocessor microcontroller microcomputer computer) same (counter timer) same compar\$4 same (threshold value) same logic same (high low) same bus	US-PGPUB; USPAT; USOCR	OR	ON	2008/03/20 15:03

S138	129	(cpu controller microprocessor microcontroller microcomputer computer) same (counter timer) same compar\$4 same (threshold value) same logic same (high low) same bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:04
S139	31	S138 and (abnormal\$4 error anamol\$4) with (sens\$4 detect\$4 measur\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:05
S140	31	S138 and (abnormal\$4 error) with (sens\$4 detect\$4 measur\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:05
S141	7	S138 and (abnormal \$4) with (sens\$4 detect \$4 measur\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:08
S142	24	S138 and (error) with (sens\$4 detect\$4 measur\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:08
S143	3	"6782500"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 17:15
S144	15	"6574760"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 17:21

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